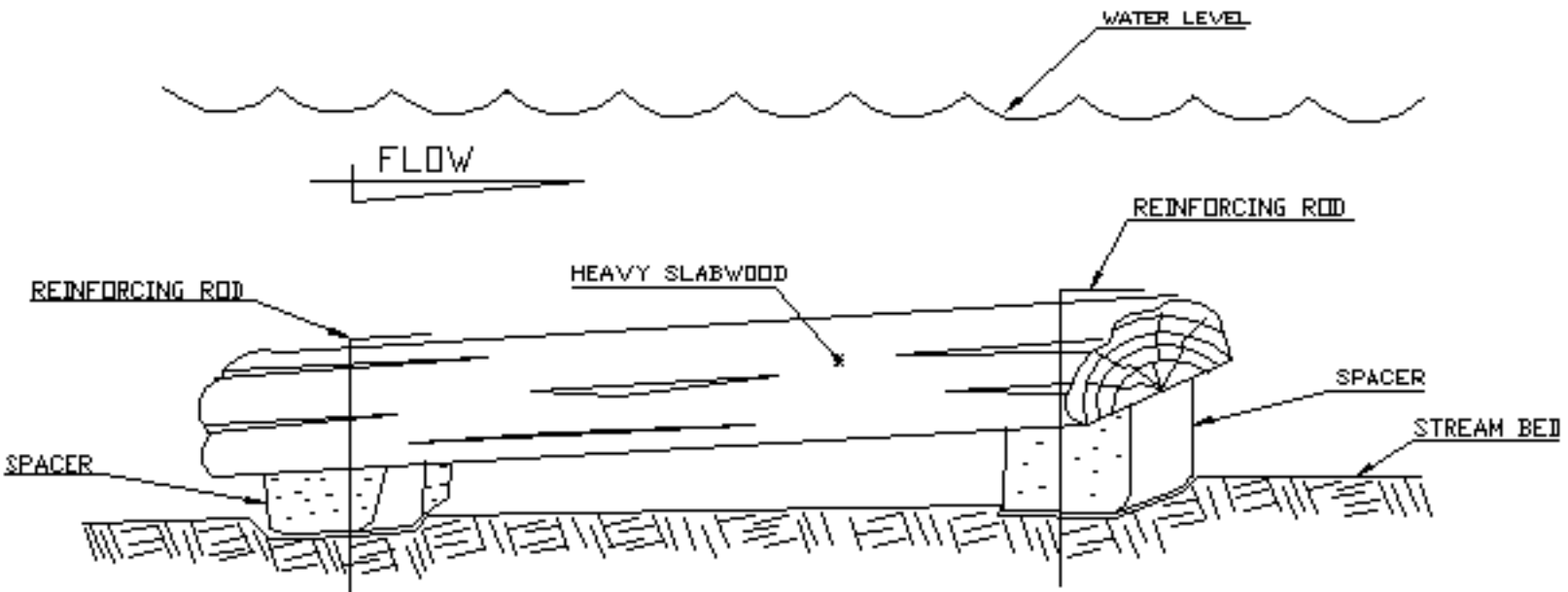


SCHEMATIC DESIGN OF

HALF-LOG DEVICE